

To: Technology Unit: 2182  
Facsimile Number: 703-872-9306


Total Pages Sent 11

From: W. Daniel Swayze, Jr.  
Texas Instruments Incorporated  
Facsimile: 972-917-4418  
Phone: 972-917-5633

RECEIVED  
CENTRAL FAX CENTER

OCT 28 2004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<b>CERTIFICATION OF FACSIMILE TRANSMISSION</b>	
I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:	
 Tommie Chambers	<u>10-28-04</u> Date

## FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET <input type="checkbox"/> NEW APPLICATION <input type="checkbox"/> DECLARATION (# Pages) <input type="checkbox"/> ASSIGNMENT (# Pages) <input type="checkbox"/> FORMAL DRAWINGS <input type="checkbox"/> INFORMAL DRAWINGS <input type="checkbox"/> CONTINUATION APP'N (# Pages) <input type="checkbox"/> DIVISIONAL APP'N	<input checked="" type="checkbox"/> AMENDMENT (10 Pages) <input checked="" type="checkbox"/> EOT (1 Month) <input type="checkbox"/> NOTICE OF APPEAL <input type="checkbox"/> APPEAL (# Pages) <input type="checkbox"/> ISSUE FEE (# Pages) <input type="checkbox"/> CHANGE IN CORRESPONDENCE <input type="checkbox"/> ADDRESS
NAME OF INVENTOR(S): Cheung	RECEIPT DATE & SERIAL NO.: <b>Serial No.: 09/965,143</b> Filing Date: 9/27/2001
TITLE OF INVENTION: METHOD AND DEVICE FOR PROVIDING A LOW POWER EMBEDDED SYSTEM BUS ARCHITECTURE	
TI FILE NO.: <b>TI-32390</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>
FAXED: <u>10-28-04</u> DUE: 10/23/2004 ATTY/SEC'Y: WDS/tlc	

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by Facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Cheung  
Serial No: 09/965,143  
Filed: 9/27/2001  
For: METHOD AND DEVICE FOR PROVIDING A LOW POWER EMBEDDED  
SYSTEM BUS ARCHITECTURE

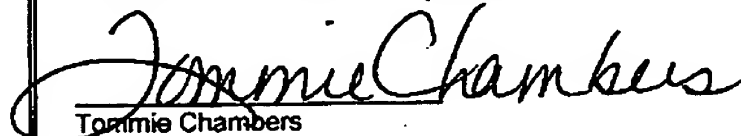
Docket No: TI-32390  
Examiner: Shin, Christopher  
Art Unit: 2182

**AMENDMENT UNDER 37 C.F.R. § 1.115**

Assistant Commissioner For Patents  
Washington, DC 20231

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being  
transmitted by facsimile to the U.S. Patent and Trademark  
Office at 703-872-9306 on 10-28-04:

  
Tommie Chambers

Dear Sir:

Responsive to the Office Action mailed July 23, 2004 in connection with the  
above identified application, Applicants respectfully submit the following amendments  
and remarks.

TI-32390 - 1 -